

FIG. 1

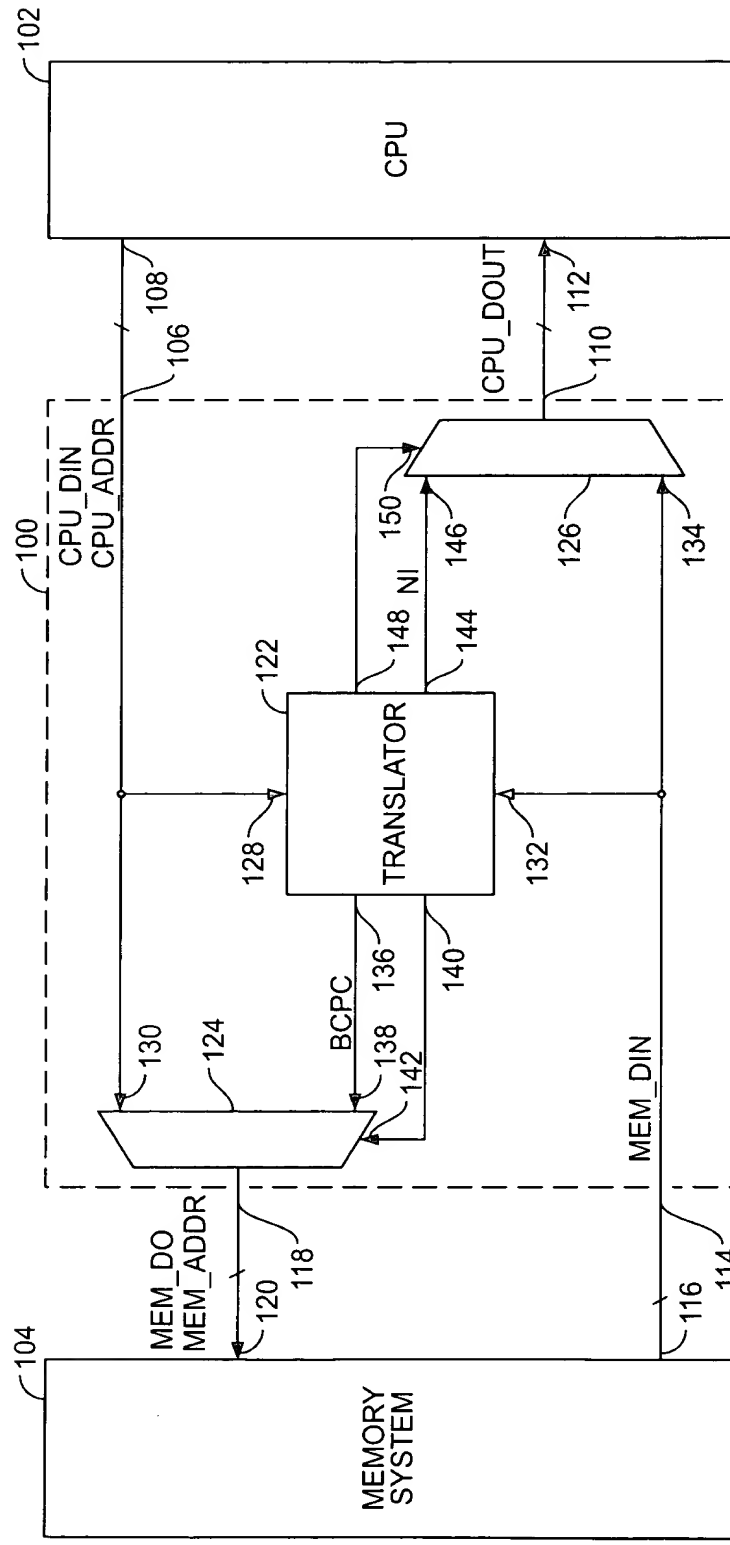


FIG. 2

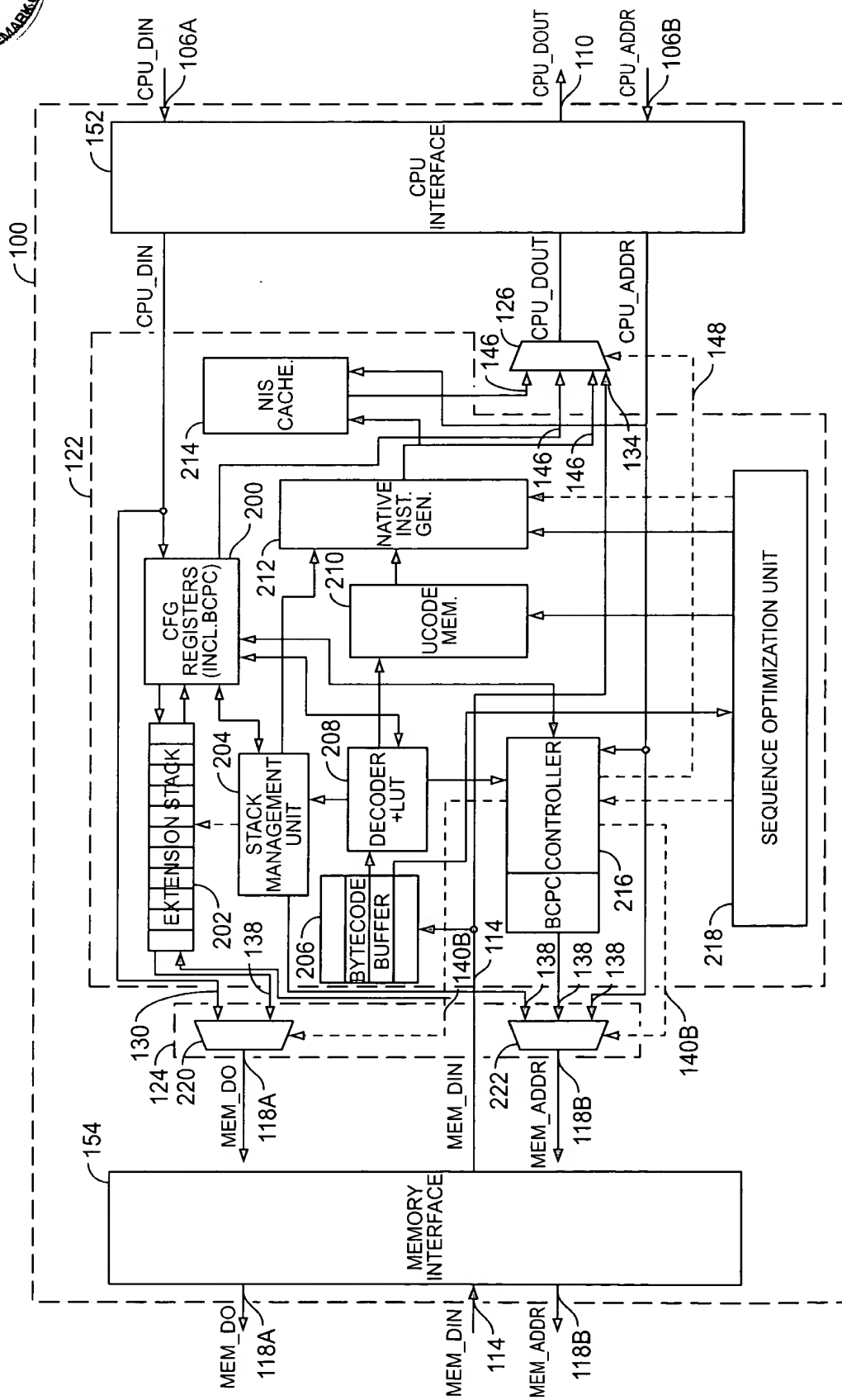


FIG. 3

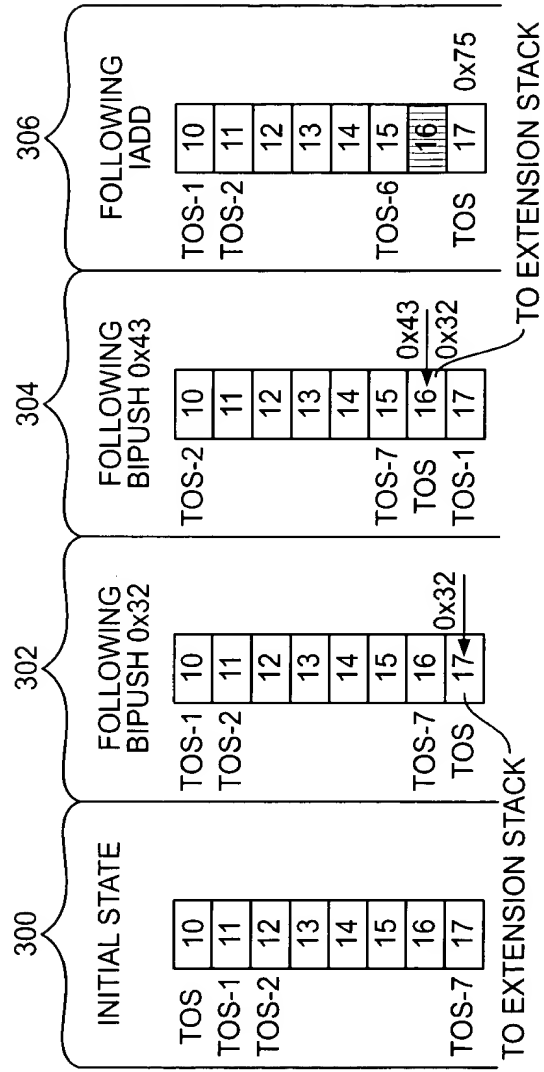


FIGURE 3 STACK BEHAVIOR

FIG. 4

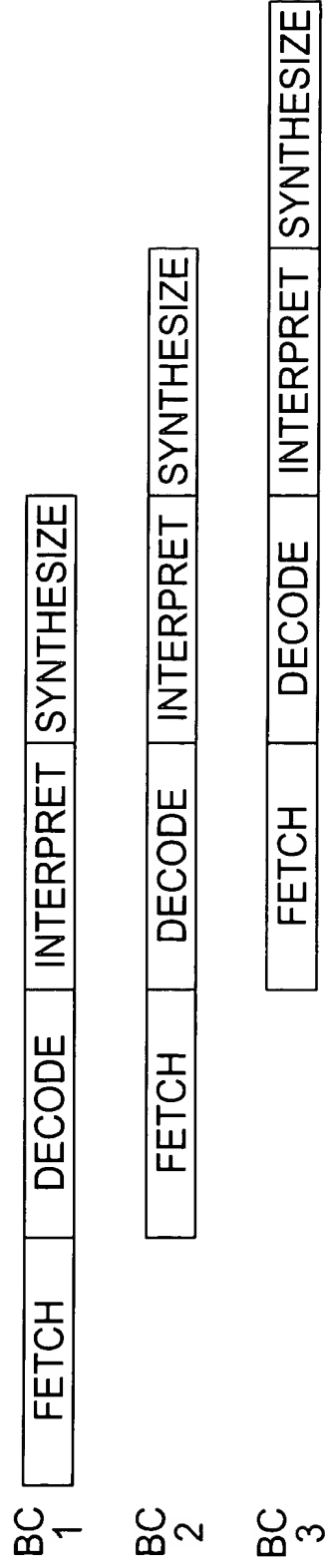


FIG. 5

	CYCLE #1	CYCLE #2	CYCLE #3	CYCLE #4	CYCLE #5	CYCLE #6	CYCLE #7	CYCLE #8	CYCLE #9	CYCLE #10
BCPC	00	04	08	36						
350	00-03	04-07	08-11							
		00	01-02	03-05	06-08					
352			00	01-02	03-05	06-08				
				00	01-02	03-05	06-08			
354				36-3A						
					37					
354	PC				1010	1011	1012	1018	1014	1015
	R					1010	1011	1012	1016	1014

FIG. 6

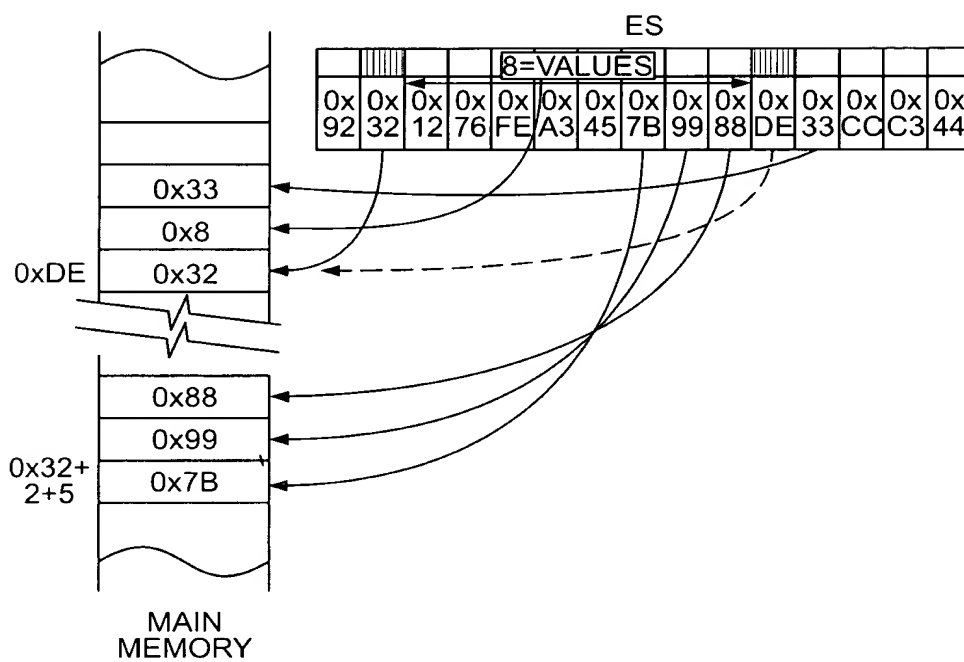


FIG. 7

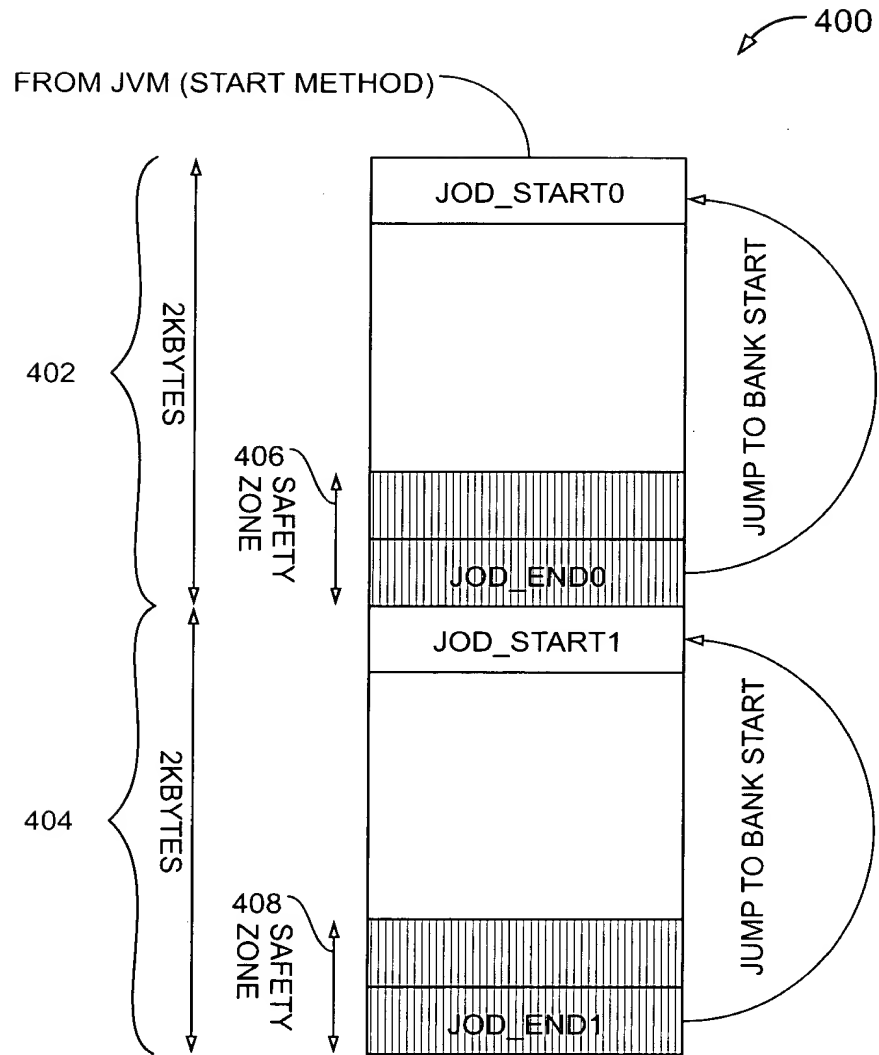


FIG. 8

	CYCLE 1	CYCLE 2	CYCLE 3	CYCLE 4	CYCLE 5	CYCLE 6	CYCLE 7+
IN							
PC	XXX	XXX	XXX		+1	+2	XXX
INST	JOP JOD_START0			JOD_START0	NOP	NOP	(*)
BCPC				Y	Y+1	Y+2	
(BCPC)				(Y)	(Y)	(Y+1)	

DETECT → CYCLE 4

(Y) TRANSLATED TO NATIVE INSTR.

FIG. 9A

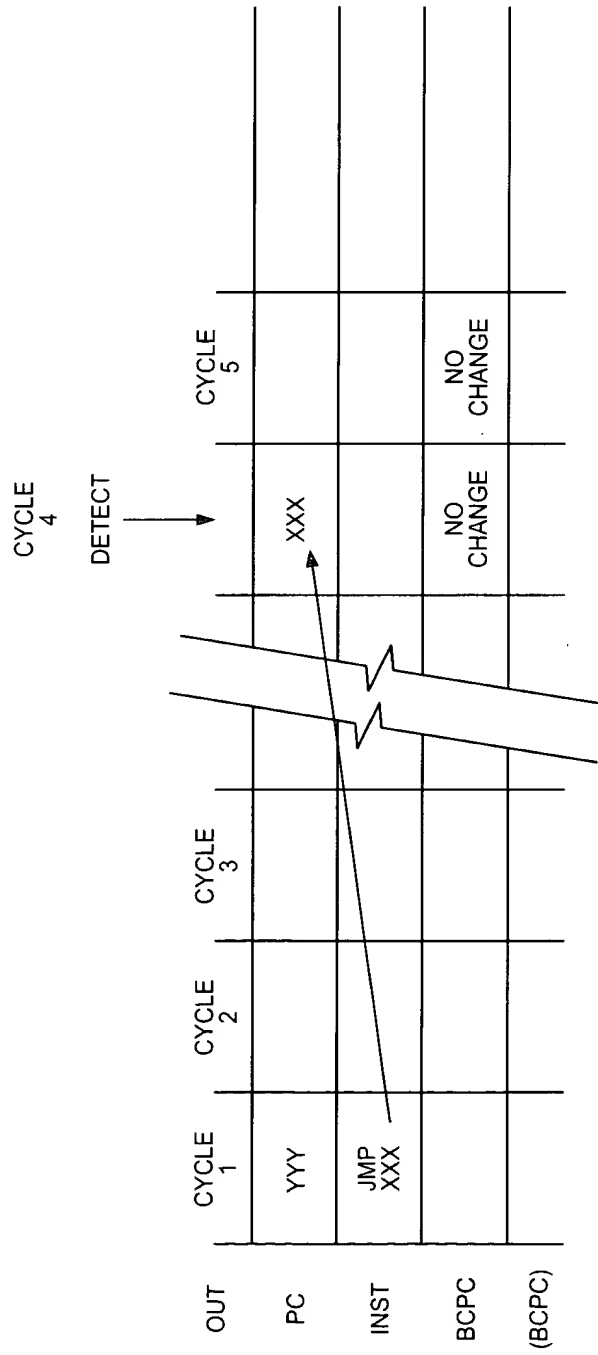


FIG. 9B

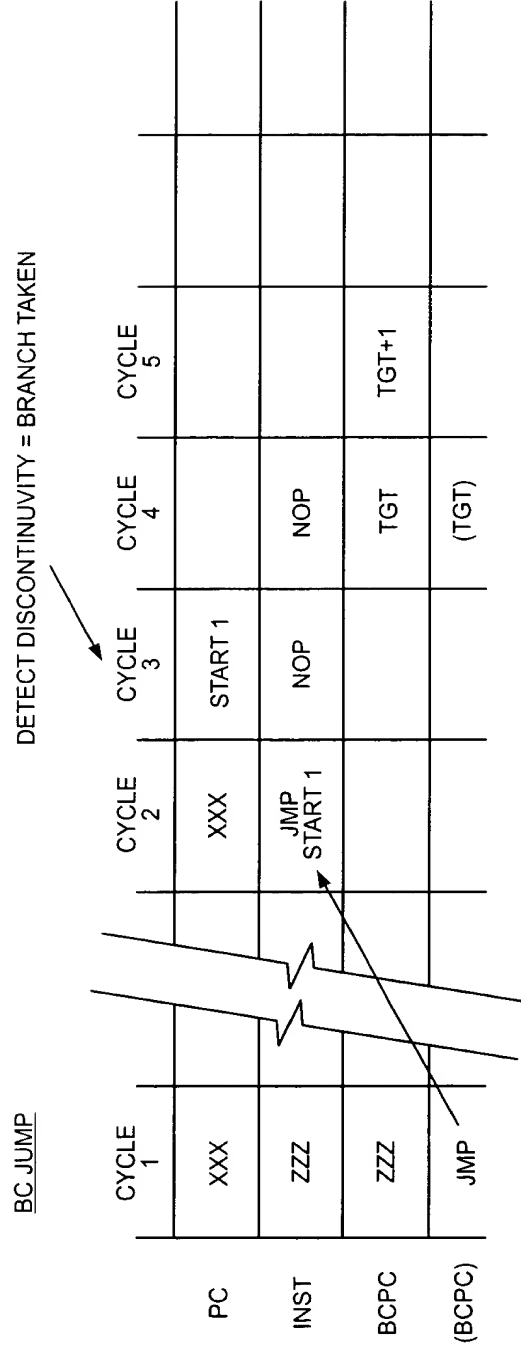


FIG. 10

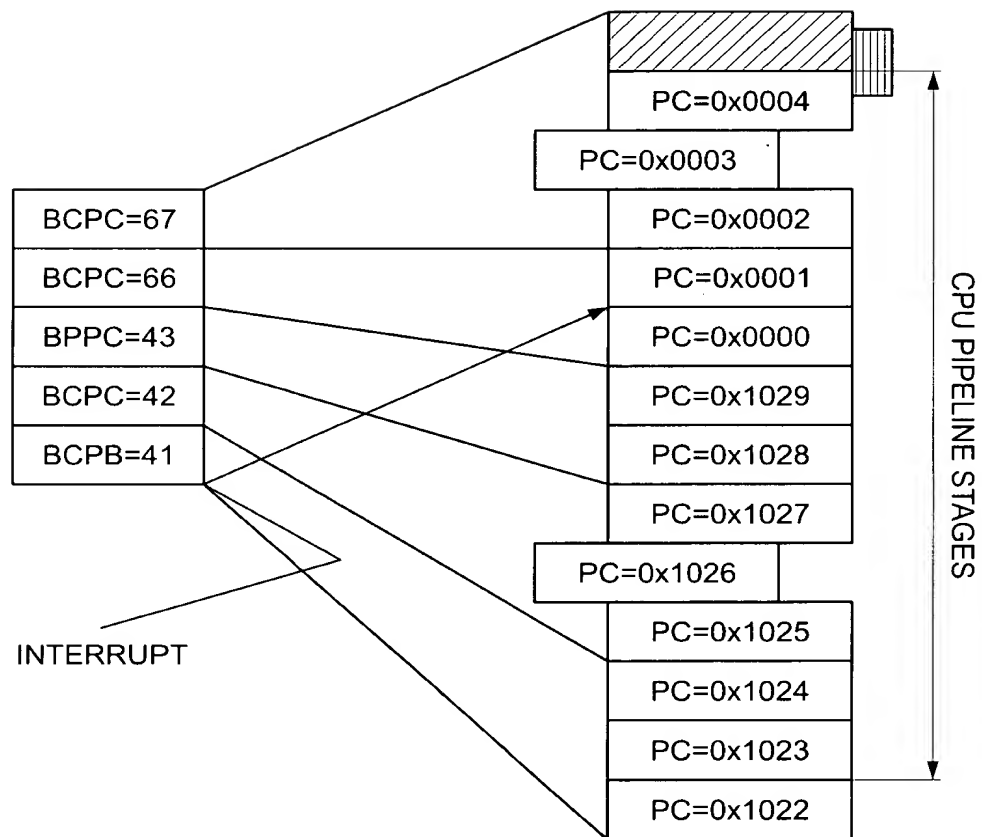


FIG. 11